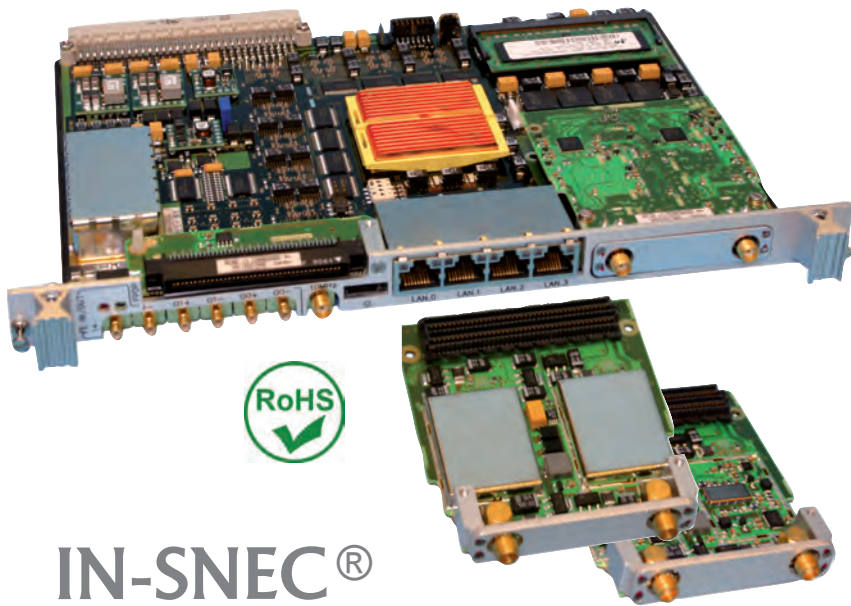


VME-IFoIP . HPC



IN-SNEC®

VME IF over IP Wideband Acquisition boards

Applications

- SDR products
- Digital communications
- Base stations
- Modems
- Wideband recording for post processing

Main Features

- 80 MHz instantaneous bandwidth
- High speed digitizer
- Wideband snapshot length up to
 - ◆ 5 s for 1 input
 - ◆ 2.5 s for 2 inputs
- 4 independent DDC up to 40MHz bandwidth
- 28 independent DDC up to 10MHz bandwidth
- Configurable RF inputs with daughter board
- API drivers

Main Benefits

- Open and evolutive architecture
- Standard interfaces
- Multiple types of RF inputs
- Modularity, powerfull for small form factor
- Easy IP

Options

- Daughter boards
 - ◆ IF=70MHz
 - ◆ IF=140MHz
 - ◆ L band
- DSP IP
 - ◆ Channelization
 - ◆ FFT

The VME-IFoIP (Intermediate Frequency over Internet Protocol) family is composed of the following VME 6U form factor boards :

- VME-IFoIP-SPC
- VME-IFoIP-HPC

The input signal is wideband digitized with an adjustable sampling frequency, channelized, formatted, optionnally processed in FPGAs. Sample flows are then routed to computer nodes over one or several LANs, using the Gigabit Ethernet interfaces. Together with the wideband acquisition board comes an easy to use library to help the users to develop their own applications.

VME-IFoIP-SPC - Standard Processing Capability Acquisition board

The VME-IFoIP-SPC wideband acquisition board is composed of one daughter board plugged on a Standard Processing Capacity mother board integrated in a VME 6U form factor within 1 slot.

This system features 2 RF analog inputs in either 70MHz or 140MHz intermediate frequency or L Band, 8 DDC, 1 Virtex-V FPGA FX100T, 4 Gigabit Ethernet.

The input signal is wideband digitized with a sampling frequency up to 186 MHz

VME-IFoIP-HPC - High Processing Capability Acquisition board

The VME-IFoIP-HPC wideband acquisition board is composed of one daughter board plugged on a High Processing Capacity mother board integrated in a VME 6U form factor within 1 slot.

This system features 2 RF analog inputs in either 70 MHz or 140 MHz intermediate frequency or L Band, up to 32 DDC, 1 Virtex-V FPGA SX240T, 4 Gigabit Ethernet.

The input signal is wideband digitized with a sampling frequency up to 186 MHz.

ZODIAC DATA SYSTEMS

AEROSAFETY & TECHNOLOGY
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Modele references

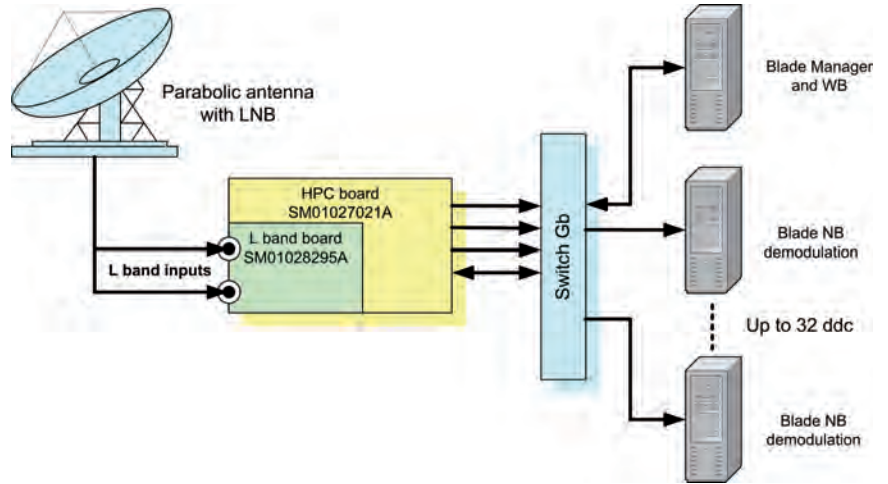
IFoIP HPC / Plug IF70	SM01026979A
IFoIP HPC / Plug IF140	SM01026980A
IFoIP HPC / Plug L Band	SM01028741A

VME-IFoIP - HPC

HPC mother board (*High Processing Capability)

WB channelizing	
Snapshot length	> 5 s for maximum bandwidth
NB channelizing	
Technique	Digital Down Converter
Channels quantity	32
Maximum BW	Plug sampling frequency/2 for 4 DDC Plug sampling frequency/8 for 28 DDC
Minimum BW	Plug sampling frequency/2000
Processing	
DDC	DDC with CIC 5 stage and up to 512 coefficients for filter (FIR)
FPGA type	XILINX V5
Datation	Yes (on master clock)
Synchronization	Internal / External (10 MHz)
Data transfer	
Mode	Data flow de-interleaving
Implementation	4 Gigabits Ethernet ports
Control	Protocol TCP/IP
Samples transfer	Protocol UDP/IP
Digital interfaces	
FPDP	Yes
I/O	Yes

Application example



Mechanical characteristics

Weight	<1.5 lb (< 500 g)			
Dimensions	VME form factor (6Ux1 slot)			
Power consumption	≤ 35W			
Temperature	operating	0 to +40°C	storage	-40 to +70°C
Relative humidity	operating	0 to +85%	storage	0 to +95%

IF=70 MHz daughter board

Analog inputs

Input quantity	2
IF signal	70 MHz maximum bandwidth 46 MHz
IF filter	SAW filter, BW@3dB = 40 MHz
Input level	Max +10 dBm
Gain /dynamic /step	26 dB /30 dB /1 dB
Noise factor	12 dB@Gmax
Level control facilities	yes

Analog to Digital conversion

Sampling frequency	93.3 MHz
ADC resolution	12 bits
SINAD	10.7 bits

Interfaces

Form factor	FMC
Connector FMC	male

IF=140 MHz daughter board

Analog inputs

Input quantity	2
IF signal	140 MHz maximum bandwidth 93 MHz
IF filter	SAW filter, BW@3dB = 80 MHz
Input level	Max +14 dBm
Gain /dynamic /step	23 dB /30 dB /1 dB
Noise factor	12 dB@Gmax
Level control facilities	yes

Analog to Digital conversion

Sampling frequency	186.6 MHz
ADC resolution	12 bits
SINAD	10.3 bits

Interfaces

Form factor	FMC
Connector FMC	male

L band daughter board

Analog inputs

Input quantity	2
RF signal	925 to 2175 MHz tunable
RF filter	Low pass filter, BW@0.5dB = 80 MHz
Input level	Max +0 dBm
Gain /dynamic /step	60 dB / 75 dB / 1 dB
Noise factor	17 dB@Gmax
Level control facilities	yes (automatic, manual)

Analog to Digital conversion

Sampling frequency	120 x 2 MHz I,Q
ADC resolution	14 bits
SINAD	> 10 bits on I and Q channel
SFDR	> 45dBc
Instantaneous dynamic	up to 31 dB in 80 MHz BW

Interfaces

Form factor	FMC
Connector FMC	male

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